Total No. of Printed Pages:1

SUBJECT CODE NO:- H-1829
FACULTY OF SCIENCE AND TECHNOLOGY
M.E. (CSE/SE)
El-1 Advanced Computer Architecture
(REVISED)

[Time: Three Hours]   **[Max.Marks: 80]**

Please check whether you have got the right question paper.

N.B
1. Solve any two questions from each section
2. Assume suitable data if necessary.

Section A

Q.1	a) Explain in brief cache memory organization.	10
	b) Explain bus systems.	10
Q.2	a) Explain different approaches of scalability analysis	10
	b) Explain Arithmetic pipeline design.	10
Q.3	a) Write a short note on super pipeline design.	10
	b) Explain SIMD inter connection network.	10

Section B

Q.4	a) Write a note on compound vector processing.	10
	b) Write a short note on three generation of multicomputer.	10
Q.5	a) Explain the parallel algorithms for array processor.	10
	b) Explain the multiprocessor scheduling strategies.	10
Q.6	a) Explain performance enhancement methods for SIMD Array processor.	10
	b) Explain the role of compiler in parallel programming.	10

1