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**SUBJECT CODE NO:- E-8225**  
**FACULTY OF ENGINEERING AND TECHNOLOGY**  
**M.E. (CSE/SE) Examination Nov/Dec 2017**  
**EI-1 Advanced Computer Architecture**  
**(Revised)**

**[Time: Three Hours]**

**[Max.Marks:80]**

Please check whether you have got the right question paper.

- N.B
- i. Solve any 2 questions from each section.
  - ii. Assume suitable data if necessary.

**Section A**

- |     |   |          |
|-----|---|----------|
| Q.1 | a) Explain Flynn's classification.<br>b) Explain different approaches of scalability analysis.                | 10<br>10 |
| Q.2 | a) Explain in detail non-linear pipeline processor.<br>b) Explain Arithmetic pipeline design.                 | 10<br>10 |
| Q.3 | a) Write a short note on SIMD array processor.<br>b) Write a note on parallel algorithm for array processors. | 10<br>10 |

**Section B**

- |     |  |                |
|-----|--|----------------|
| Q.4 | a) What is associate array processing and array processors?<br>b) Write some application areas where vector processing is important. | 10<br>10       |
| Q.5 | a) Explain cache coherence and synchronization mechanism.<br>b) Explain two models for communication in multiprocessor systems.      | 10<br>10       |
| Q.6 | Write short notes on-<br>i) VLIW architecture<br>ii) RISC/CISC processors<br>iii) Parallel processing languages                      | 07<br>07<br>06 |